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NOTICE OF ALLOWANCE AND FEE(S) DUE

24737

7590

11/19/2008

PHILIPS INTELLECTUAL PROPERTY & STANDARDS P.O. BOX 3001 BRIARCLIFF MANOR, NY 10510 EXAMINER

NHU, DAVID

ART UNIT

PAPER NUMBER

2895

DATE MAILED: 11/19/2008

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/596,380	06/12/2006	Steven C. Deane	GB030221	5921

TITLE OF INVENTION: ACTIVE MATRIX PIXEL DEVICE WITH PHOTO SENSOR

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0	\$1810	02/19/2009

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE
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appropriate. All further	correspondence including ed below or directed oth	ng the Patent, advance of herwise in Block 1, by (orders and notification	of m	naintenance fees w	ill be	mailed to the current	correspondence addre	ess as
		lock 1 for any change of address)		Fee(s) Transmittal. Thi rs. Each additional	s certif l paper	g can only be used for ficate cannot be used for such as an assignment filing or transmission.	or any other accompan	nying
PHILIPS INTE P.O. BOX 3001 BRIARCLIFF N	NDARDS		Certificate of Mailing or Transmission I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (571) 273-2885, on the date indicated below.						
								(Depositor's	name)
								(Sign	nature)
									(Date)
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CFR 1.363). Change of corresp Address form PTO/SI "Fee Address" ind PTO/SB/47; Rev 03-0 Number is required. 3. ASSIGNEE NAME A	(2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.								
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4a. The following fee(s) Issue Fee Publication Fee (N Advance Order -	4b. Payment of Fee(s): (Please first reapply any previously paid issue fee shown above) A check is enclosed. Payment by credit card. Form PTO-2038 is attached. The Director is hereby authorized to charge the required fee(s), any deficiency, or credit any overpayment, to Deposit Account Number (enclose an extra copy of this form).								
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24737 75	90 11/19/2008		EXAMINER		
PHILIPS INTEL	LECTUAL PROPER	NHU, DAVID			
P.O. BOX 3001		ART UNIT	PAPER NUMBER		
BRIARCLIFF MA	NOR, NY 10510	2895			
		DATE MAILED: 11/19/2008			

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 0 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 0 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

DEANE, STEVEN Art Unit 2895 the correspondence address application. If not include a cation will be mailed in due of ject to withdrawal from issues.	dress ded e course. THIS
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REASONS FOR ALLOWANCE

1. Claims 5, 7-11 are allowed.

The following is an examiner's statement of reasons for allowance: None of the references of record teaches or suggests as cited in claims 5, 7: An active matrix pixel device comprising a plurality of polycrystalline silicon islands supported by a substrate, one of the polycrystalline silicon islands providing a channel and doped source/drain regions of a thin film transistor, the active matrix pixel device further comprising a PIN diode which includes a p-type doped region and an n-type doped region separated by an amorphous silicon intrinsic region, wherein the amorphous silicon intrinsic region overlies and contacts at least a part of one of the polycrystalline silicon islands which provides one of the p-type or n-type doped regions of the PIN diode, wherein the doped source/drain regions and said one of the p-type or n-type doped regions of the PIN diode are provided by the same polycrystalline silicon island and a vertical n-i-p stack is used by using a doped region of a polysilicon thin film transistor (TFT) for the ntype doped region (as cited in claim 5); An active matrix pixel device comprising a plurality of polycrystalline silicon islands supported by a substrate, one of the polycrystalline silicon islands providing a channel and doped source/drain regions of a thin film transistor, the active matrix pixel device further comprising a PIN diode which includes a p-type doped region and an n-type doped region separated by an amorphous silicon intrinsic region, wherein the amorphous silicon intrinsic region overlies and contacts at least a part of one of the polycrystalline silicon islands which provides one of the p-type or n-type doped regions of the PIN diode, wherein both the p-type and n-type doped regions of the PIN diode are provided by respective ones of the polycrystalline silicon islands, and wherein a distance between the pArt Unit: 2895

type and n-type doped regions is substantially equal to a gap between adjacent thin film transistors (TFTs) (as cited in claim 7).

3. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

- 4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure: Young (2006/0030084): manufacture of Electronic Devices Comprising Thin Film Circuit Elements.
- 5. Any inquiry concerning this communication on earlier communications from the examiner should be directed to David Nhu, (571)272-1792.

The examiner can normally be reached on Monday-Friday from 7:00 AM to 5:30 PM.

The fax phone number for the organization where this application or proceeding is assigned is (571)273-8300.

/David Nhu/ 11/15/2008 DN

Primary Examiner, Art Unit 2895